AUG 15 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Andrew E. Horch

Serial No.: 10/639,058

Filed: August 11, 2003

Title: Thyristor Semiconductor Memory

Device and Method of Manufacture

Group Art Unit: 2814

Examiner: Vikki H. Trinh

Confirmation No. 6760

Atty. Docket: 2000.004.00/US

Date: August 15, 2005

SUPPLEMENTAL AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 CERTIFICATE OF FACSIMILE SUBMISSION 37 CFR 1.8

I hereby certify that this correspondence is being facsimile transmitted to the United States Postal Service to Fax No. 571-273-1719, Commissioner for Patents, P.O. Box 1450, Alexandrin, VA 22313-1450, on August 15, 2005

Walter D. Rinkle

Dear Sir:

Applicant submits this amendment responsive to the Examiner's informality inquiry of earlier today and supplemental to applicant's previous amendment of June 20, 2005, which was submitted responsive to the Office Action dated March 30, 2005.

Amendment to the Specification begins on page 2.

Remarks/Arguments begin on page 3.